D. REMARKS

Claims 1-55 are pending in the application. The examiner has rejected claims 1-9, 17-22, 29-37, and 45-50 under 35 U.S.C. § 102(b) as being anticipated by Chen, U.S. Patent No. 5,226,723. The examiner has rejected claims 10-16 and 38-44 under 35 U.S.C. § 103(a) as being unpatentable over Chen in view of Telefonbau, German Publication No. 1 099 403. The examiner has rejected claims 23-28 and 51-55 under 35 U.S.C. § 103(a) as being obvious over Chen in view of Caldwell, U.S. Patent No. 5,594,222. The examiner has objected to claims 1 and 29 based on matters of form. Finally, the examiner has objected to the drawings under 37 C.F.R. § 1.83(a) on the ground that they do not show each and every feature set forth in the claims. Applicant hereby amends claims 1, 17-20, 29, and 45-48 and Fig. 1B. Applicant also amends the specification to conform to the amended drawing.

1. The Rejections Under 35 U.S.C. § 102(b) Have Been Overcome.

Claim 1, as amended, recites:

An integrated low profile display, comprising:

a substrate having a first surface and a second surface;

said substrate defining at least one penetration extending through said substrate from said first surface to said second surface;

each said penetration having a side wall, an entrance opening defined by said first surface, and an exit opening defined by said second surface; and

at least one light emitting device;

each said light emitting device mounted to said first surface of said substrate proximate the entrance opening of a corresponding penetration and adapted to selectively admit light to said penetration via said entrance opening.

Applicants respectfully submit that Chen does not disclose the foregoing combination of elements. Importantly, Chen does not disclose "a substrate defining at least one penetration" and "at least one light emitting device . . . mounted to said first surface of said substrate proximate the entrance opening of a corresponding penetration." Instead, Chen discloses a plurality of light emitting diodes (3) mounted to a printed circuit board (1) and a separate reflector unit (2) mounted to the printed circuit board. Reflector unit (2) has a first plurality of penetrations (21), each of which corresponds to a respective light emitting diode (3) and through which a corresponding bonding wire (31) is routed, and a second plurality of penetrations 22 through which such corresponding bonding wire (31) is further routed. Printed circuit board (1) and reflector unit (2) must be prepared with great precision so that light emitting diodes (3) properly align with corresponding penetrations (21) when printed circuit board (1) and reflector (3) are later joined together. The integrated low profile display recited in Applicants' claim 1 overcomes this distinct disadvantage of the prior art.

Claim 29 recites a substrate defining at least one cavity having an entrance opening and a closed end. Applicants respectfully submit that Chen does not disclose such a cavity. Indeed, Chen could not even be modified to include such a cavity because such modification would make it impossible to route bonding wire (31) as taught by Chen.

For at least the foregoing reasons, Applicants respectfully submit that claims 1 and 29 are allowable over the cited prior art. Because claims 2-28 and 30-55 depend from claims 1 and 29, respectively, Applicants respectfully submit that these claims are allowable as well.

2. The Rejections Under 35 U.S.C. § 103(a) Are Moot.

Applicant respectfully submits that the rejections of dependent claims 10-16, 23-28, and 38-44, and 51-55 under 35 U.S.C. § 103(a) are moot because each of these claims depends from an allowable base claim.

3. The Objections to Claims 1 and 29 Have Been Overcome.

Applicant respectfully submits that amended claims 1 and 29 are written in proper form and overcome the examiner's objection.

4. The Objections to the Drawings Have Been Overcome

The examiner has objected to the drawings on the grounds that they do not show the "user interface panel" recited in claims 5 and 33, the "sensor" recited in claims 24 and 51, the "first electrode" recited in claims 25 and 52, the "second electrode" recited in claims 26 and 53, the "active component" recited in claims 27 and 54, or the "integrated control circuit of claims 28 and 55.

Applicants respectfully traverse the objection with respect to the "interface panel" recited in claims 5 and 33. Each of these claims recites "said substrate comprises a user interface panel." Reference numeral 12 identifies such substrate and, therefore, such user interface panel in FIGS 1A, 1B, 2, and 3. Likewise, reference numeral 112B identifies such substrate and, therefore, such user interface panel in FIG. 4.

Applicants submit herewith a replacement drawing sheet including FIGS. 1A, 1B, 2, 3, and 4. Fig. 1B has been amended to show a sensor (40) having first and second electrodes (42,44), and an active component or an integrated control circuit (46). Applicants also hereby amend the specification as necessary to include reference numerals for the foregoing components. Applicants respectfully submit that as-filed claims 24-28 and 51-55 and corresponding portions of the

specification provide support for the foregoing drawing and specification amendments and that no new matter has been added.

Respectfully submitted,

Mark P. Vrla

Registration No. 43,973

Dated: April 13, 2005

JENNER & BLOCK LLP One IBM Plaza Chicago, IL 60611 Ph: (312) 923-2657

fax: (312) 840-7657

C. Amendments to the Drawings

Please enter amended drawing FIG. 1B, a copy of which is attached to this paper. This drawing has been amended to show the features recited in as-filed claims 24-28, namely, a sensor 40, a first electrode 42, a second electrode 44, and an active component or an integrated circuit 46.